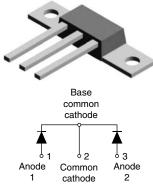
Vishay High Power Products

Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A



D-61-8

2 x 40 A

35 to 45 V

PRODUCT SUMMARY

I_{F(AV)}

 V_R

FEATURES

- 150 °C T_J operation
- Center tap module
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mould low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	80	A		
V _{RRM}	Range	35 to 45	V		
I _{FSM}	t _p = 5 μs sine	5800	A		
V _F	40 Apk, T _J = 125 °C (per leg)	0.47	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	80CNQ035APbF	80CNQ040APbF	80CNQ045APbF	UNITS
Maximum DC reverse voltage V _R		- 35	40	45	V
Maximum working peak reverse voltage	V _{RWM}		40	45	v

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	. TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current per leg	F(A)/)	50 % duty cycle at T _C = 114 °C, rectangular waveform		40		
See fig. 5 per device				80		
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	5800	A	
non-repetitive surge current per leg See fig. 7		10 ms sine or 6 ms rect. pulse		750		
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 8 \text{ A}, L = 1.7 \text{ mH}$		54	mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		8	А	

* Pb containing terminations are not RoHS compliant, exemptions may apply





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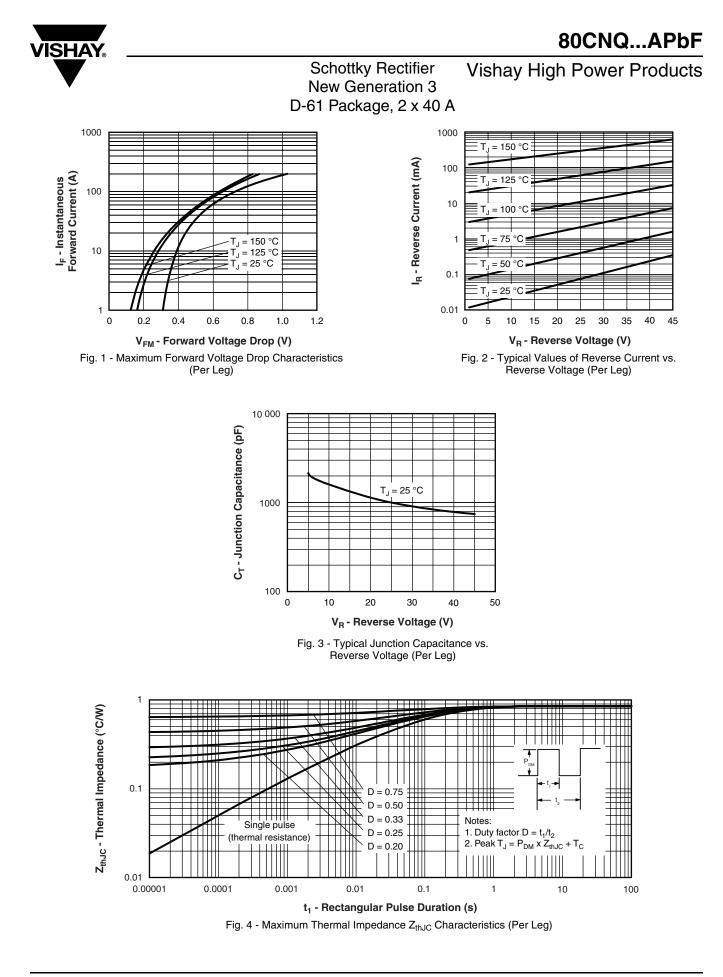


ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
	V _{FM} ⁽¹⁾	40 A	T, = 25 °C	0.52	V
Maximum forward voltage drop per leg		80 A	1j=25 C	0.66	
See fig. 1		40 A	T _J = 125 °C	0.47	
J		80 A		0.61	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	5	mA
See fig. 2	'RM `´	T _J = 125 °C		250	
Threshold voltage	V _{F(TO)}	T T maximum		0.26	V
Forward slope resistance	r _t	$T_{\rm J} = T_{\rm J} \text{ maximum} $ 3.93			mΩ
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C 2600			pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.5 nH			nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/μs			V/µs

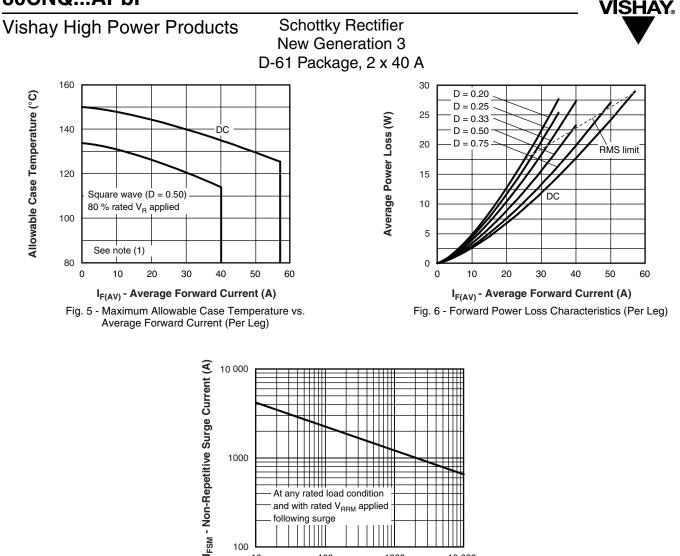
Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance,	per leg	- R _{th.IC}	DC operation See fig. 4	0.85	°C/W	
junction to case	per package		DC operation	0.42		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approvimate weight				7.8	g	
Approximate weight				0.28	oz.	
Mounting torque	minimum			40 (35)	kgf ⋅ cm	
Mounting torque	maximum			58 (50)	(lbf ⋅ in)	
Marking device				80CNQ035A		
			Case style D-61	80CNQ040A		
				80CNQ045A		



80CNQ...APbF



t_p - Square Wave Pulse Duration (μs)

1000

10 000

100

10

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

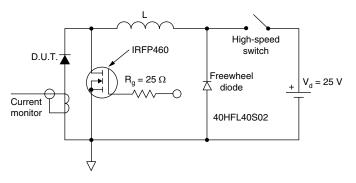


Fig. 8 - Unclamped Inductive Test Circuit

Note

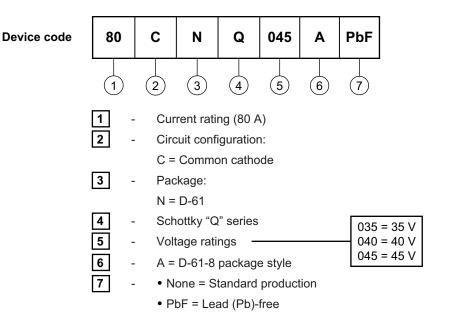
- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 D); } \mbox{I}_{R} \mbox{ at } \mbox{V}_{R1} = 80 \ \% \mbox{ rated } \mbox{V}_{R} \end{array}$



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ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95019				
Part marking information	http://www.vishay.com/doc?95030			



Vishay

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